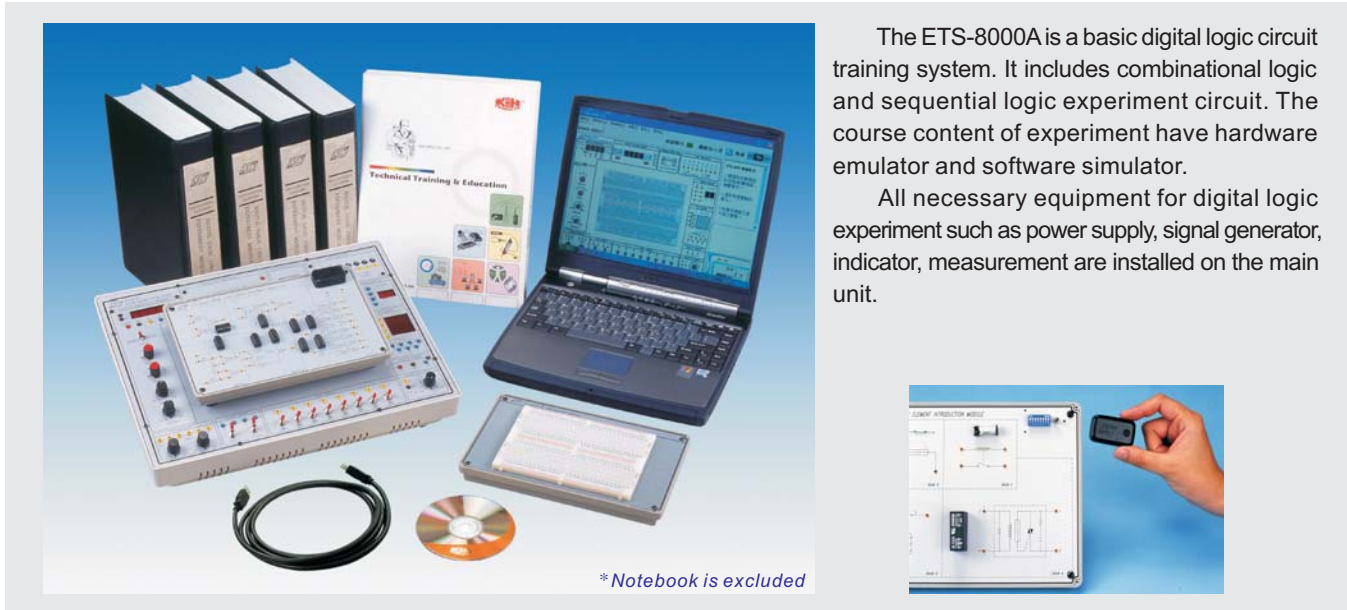




ETS-8000A

General Digitized Training System



*Notebook is excluded

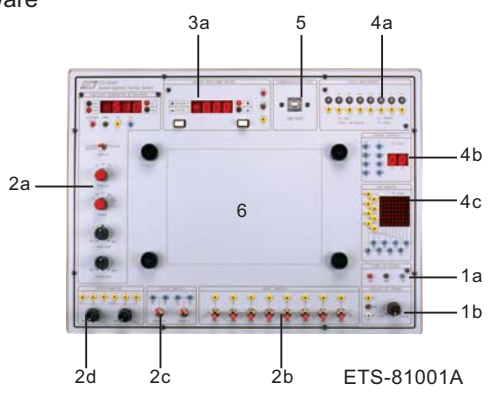
The ETS-8000A is a basic digital logic circuit training system. It includes combinational logic and sequential logic experiment circuit. The course content of experiment have hardware emulator and software simulator.

All necessary equipment for digital logic experiment such as power supply, signal generator, indicator, measurement are installed on the main unit.



Features

- Suitable for combinational logic, sequential logic experiments and designs
- Ideal tool for learning the basics of digital logic circuits
- Comprehensive power, signal supply and measurement devices for making experiments easily
- Expandable and flexible experiments with the included universal breadboard
- All supply units are equipped with overload protection for safety
- Computer interaction includes simulation software & emulation hardware



Specification

Main Unit (ETS-81001A)

1. Power Supply Units

- a. Fixed DC power supply
 - Voltage range : +5 V, -5 V
 - Maximum current output : 0.3 A
 - With overload protection
- b. Dual adjustable DC power supply
 - Voltage range : $\pm 3 \text{ V} \sim \pm 18 \text{ V}$, continuously adjustable
 - Maximum current output : 1 A
 - With overload protection

2. Signal Generator Units

- a. Function generator
 - Output waveform : sine, triangle, square, pulse
 - Output frequency : 1~100 KHz; 5 settings, continuously adjustable
 - Output impedance : 50Ω
 - Output amplitude : $\geq 18 \text{ Vpp}$ (open loop); $\geq \text{Vpp}$ (with 50Ω load)
 - Digital display : 4 sets of 7-segment LED display
 - With Hz, KHz, gate, OVFL LED
 - With frequency counter
 - Minimum input voltage : 300m Vpp
 - Counter range : DC ~ 100 KHz
- b. Data switch
 - 8 sets of independent output
 - Output level : TTL
 - Fan out : 10 TTL load
- c. Pulse switch
 - 2 sets of independent control output
 - Each set with Q, \bar{Q} output, pulse width > 5 ms
 - Output Level : TTL
 - Each set of switch with debounce circuit
 - Fan out : 10 TTL load
- d. Potentiometer
 - 1 KΩ, 0.25W, variable resistor with 3 terminals (1,2,3,) with overload protection
 - 100 KΩ, 0.25W, variable resistor with 3 terminals (1,2,3,) with overload protection

3. Measurement Units

- a. 3 1/2-digit digital volt/amp meter
 - DC voltage range : 2 V, 20V
 - DC voltage accuracy : $\pm (0.3\% \text{ of reading} + 1 \text{ digit})$
 - DC current range : 2 mA, 2 A
 - DC current accuracy : $\pm (0.5\% \text{ of reading} + 1 \text{ digit})$

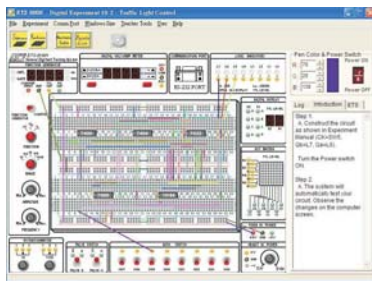


4. Indicator Units

- a. Logic indicators
 - Logic level : TTL
 - Display : Red LED for logic high, green LED for logic low; open status is none
 - 8 sets of independent input terminal
- b. Digital display
 - Logic level : TTL
 - 2 sets of independent 7-segment LED display
 - With BCD, 7-segment decode/driver input terminal
- c. 8x8 LED dot matrix
 - Logic level : TTL
 - With row input terminal : R1~R8
 - With column input terminal : C1~C8

5. System requirements

Hardware : CPU PIII 300MHz, RAM 128MB, HDD 100 MB
free space or better
Software : Windows 98/ 2000/XP



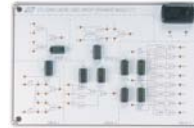
- a. Software simulation
 - Simulate all the active status of digital logic circuits on the platform of ETS-8000A
 - With simulation software of breadboard
 - Simulate all digital experiments from user manual
 - Hint for experiment procedure
 - Automatically judge the line connection is true or false by computer
 - Record experiment result
- b. Hardware emulation
 - Receive signal status of ETS-8000A platform through USB
 - Display the entity operation of ETS-8000A platform in screen
 - Display and hint for how to connect & proceed from screen
 - Automatically judge the experiment result by software
 - Record experiment result

► Experiment Modules

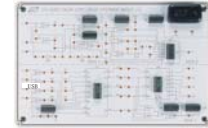
1. Each module is equipped with an 8-bit DIP switch for fault simulations. Students can practice trouble shooting by setting the DIP switch to different positions.
2. All terminals on the modules accept 2 mm plugs.
3. Comprehensive experiment manual
4. Module dimension : 255 x 165 x 30 mm
5. Individual storage case for each module

► List of Modules

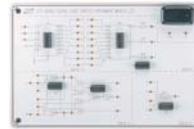
- ETS-83001 Digital Logic Circuit Experiment Module (1)
ETS-83002 Digital Logic Circuit Experiment Module (2)
ETS-83003 Digital Logic Circuit Experiment Module (3)
ETS-83004 Digital Logic Circuit Experiment Module (4)



ETS-83001



ETS-83003



ETS-83002



ETS-83004

► List of Experiments

- Basic logic gates experiments
 - (a) OR gate
 - (b) NOT gate
 - (c) OR + NOT gate
 - (d) NOR gate
 - (e) NAND gate
 - (f) 4-inputs NAND gate
 - (g) AND-NOR
 - (h) Staircase lamp
- Assembled logic circuits experiments
 - (a) $X+0=X$, $X+1=1$
 - (b) $X \cdot 0=0$, $X \cdot 1=X$
 - (c) $X+X=X$, $X+X'=1$
 - (d) $X \cdot X=X$, $X \cdot X'=0$
 - (e) $(X \cdot Y)'=X'+Y'$
 - (f) $(X+Y)'=X' \cdot Y'$
 - (g) 2-bits comparator
 - (h) Voting circuit
 - (i) Karnaugh map application
- Adder/subtractor experiments
 - (a) Half adder
 - (b) Full adder
 - (c) Half subtractor
 - (d) Full subtractor
 - (e) 4-bits adder
 - (f) 4-bits subtractor
 - (g) BCD code adder
- Decoder/encoder experiments
 - (a) 8 to 3 encoder
 - (b) 3 to 8 decoder
- Multiplexer experiments
 - (a) Multiplexer
- Basic Flip-Flop experiments
 - (a) Constructing a R-S Flip-Flop with NAND gates
 - (b) Constructing a R-S Flip-Flop with NOR gates
 - (c) J-K Flip-Flop
 - (d) T Flip-Flop
 - (e) D Flip-Flop
- Application of Flip-Flop experiments
 - (a) Constructing a D Flip-Flop with a J-K Flip-Flop;
 - (b) Constructing a T Flip-Flop with a J-K Flip-Flop;
 - (c) Mod-8 of ripple counter
- Counter experiments
 - (a) Mod-8 counter
 - (b) Mod-4 arbitrarily sequence
- Application of digital logic
 - (a) 0~9 electric roulette
 - (b) Traffic sign control

► Accessories

1. AC cord
2. Anti-dust cover
3. Experiment manual
4. Connection plugs : $\varnothing 2$ mm, 10 mmL
5. Connector leads : 1 set
6. CD : Software for data acquisition
7. USB cable